

## ABSTRACT

A path search processing section 110 of a searcher 108 performs despread processing for a signal that has undergone AAA reception by an MF 110a. A power addition circuit 110c adds power of a signal in which coherent addition of a despread signal has been performed, and outputs the result to a delay profile circuit 110d and also to a threshold value decision circuit 111. Outputs from respective threshold value decision circuits 111 are output to a combining circuit 112. A power combination value combined by the combining circuit 112 is output to a delay profile circuit 113, and a delay profile is created based on this power combination value. A finger assignment circuit 114 finds despread timing for AAA reception weight control from the delay profile.

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